



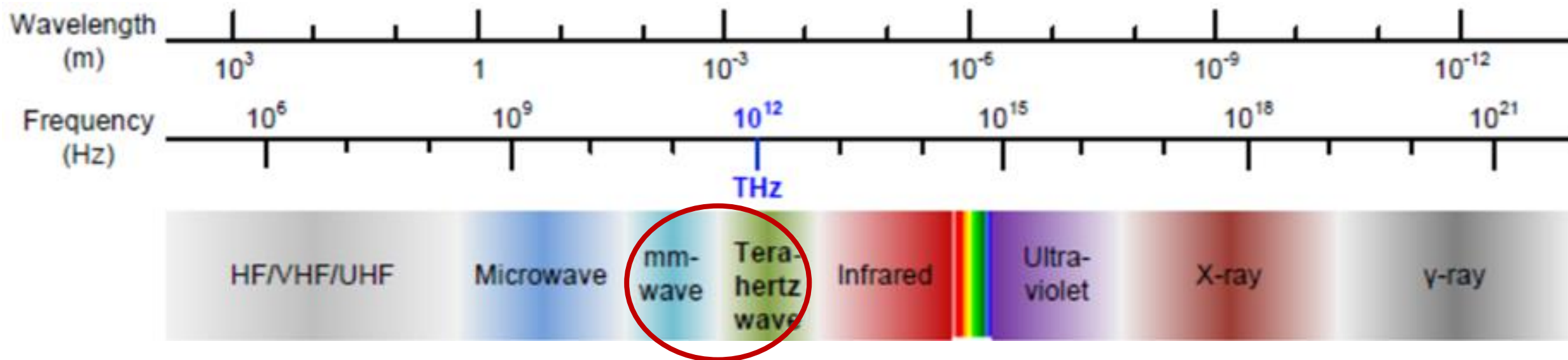
Millimeter-wave Electronics

XU, Dong

Compound Semiconductor Division, SITRI, Shanghai

November 12, 2019

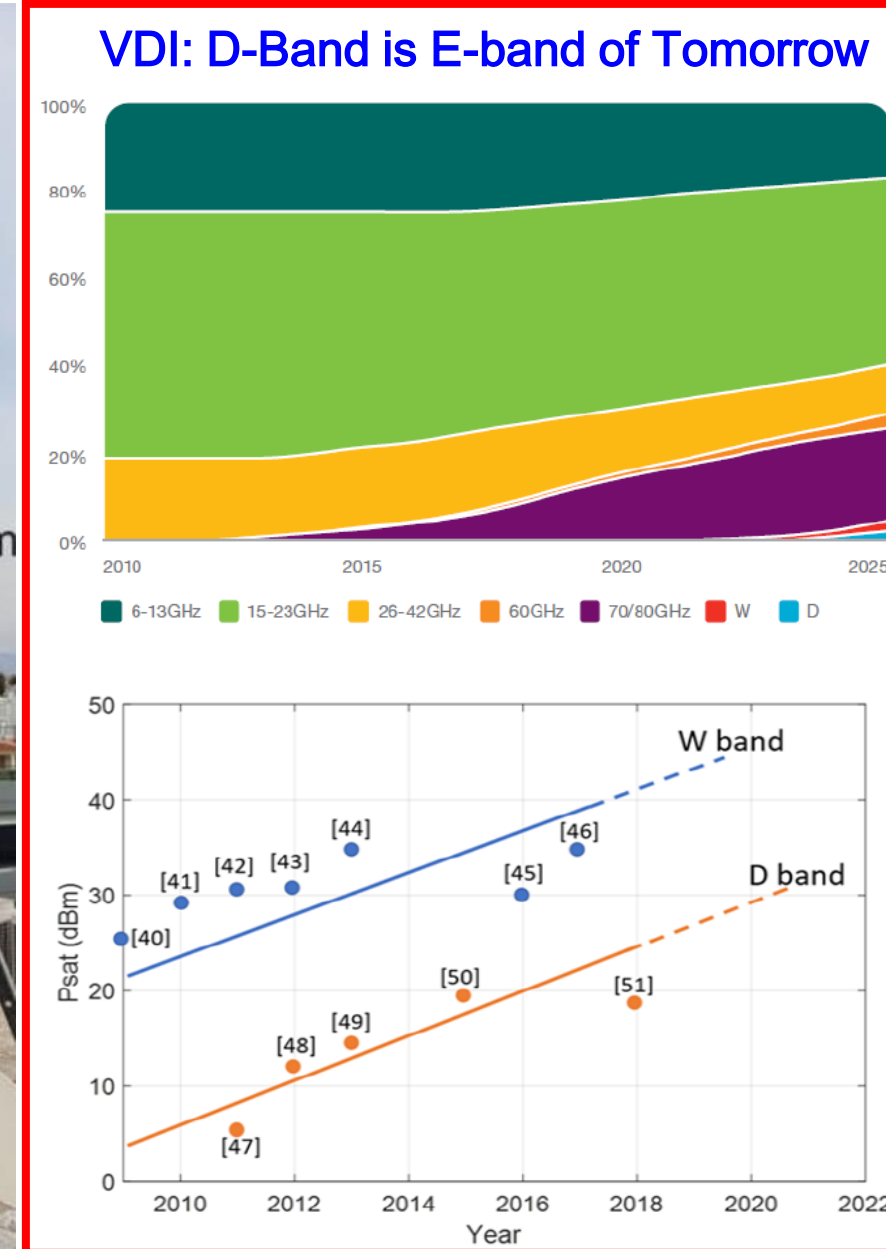
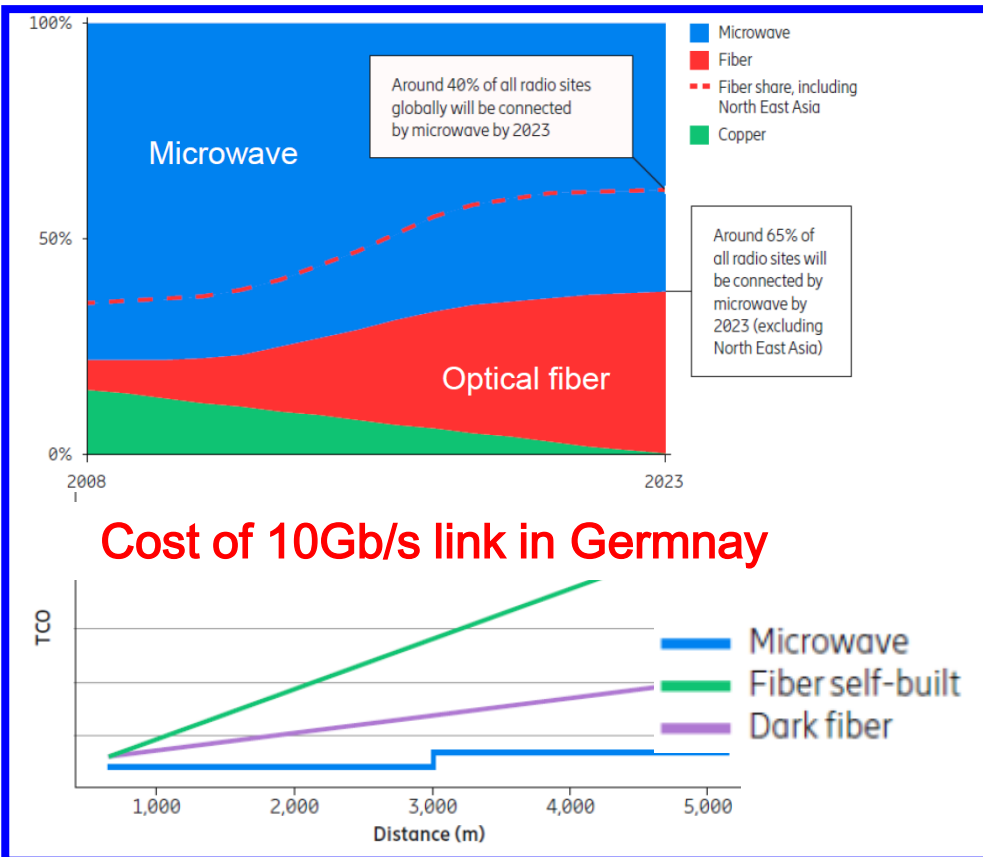
MMW, Sub-MMW and Tera Hertz



- High frequency: Wide bandwidth for high-speed RF data link and wireless communication
- Short wavelength: High precision Radar/imaging, and compact antenna-integrated RF module
- Low photon energy: Non-destructive and safe to human body, security/biomedical imaging
- THz frequencies: Corresponding to vibrational/rotational transition energies of bio-molecules
- “THz Gap”: Hard-to-reach and, therefore, remains unexploited so far:
 - Electronics: Operation speed of FET and HBT limited by line widths and layer thickness
 - Optical devices: Energy gaps corresponding to such long wavelengths too small

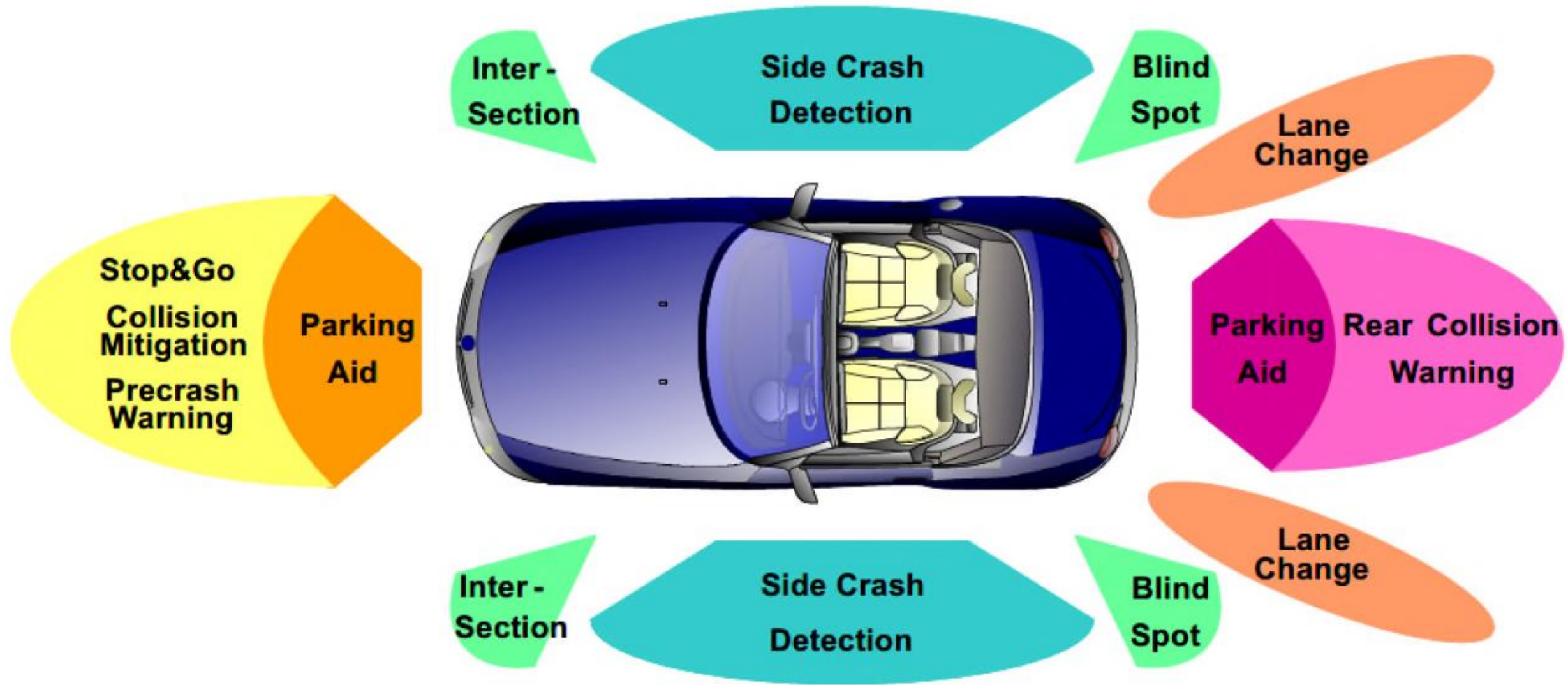


"All RF Data Link" : Ericsson



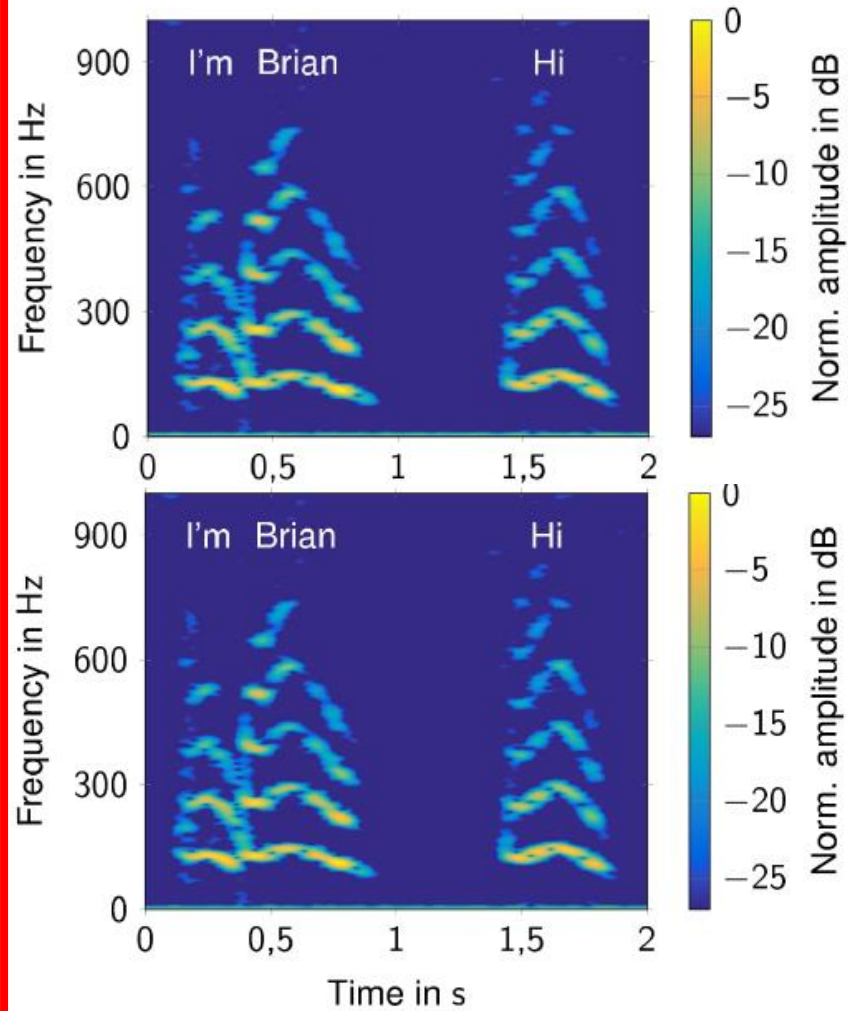


Automobile Radar Sensors

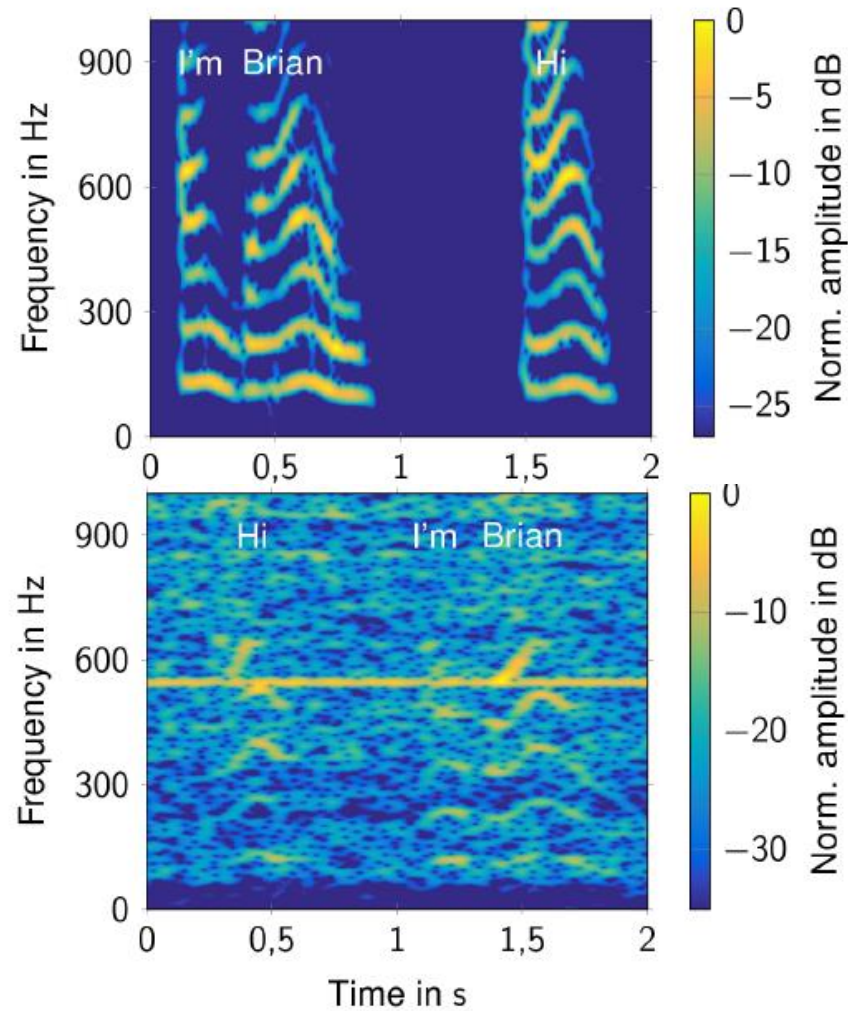


Exotic Applications

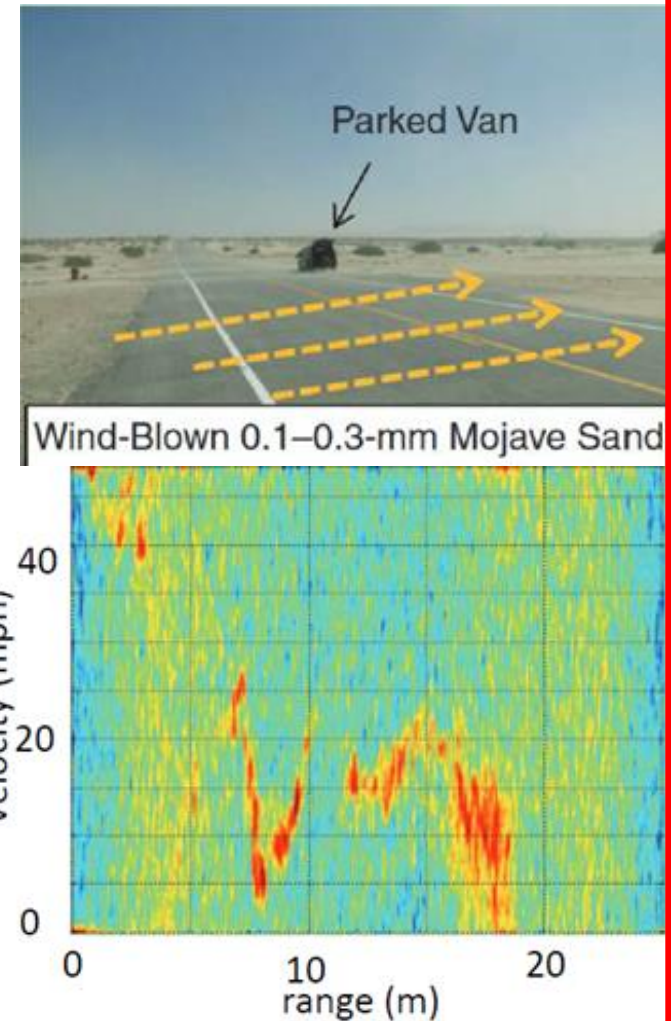
160-GHz Radar Microphone



Reference Microphone

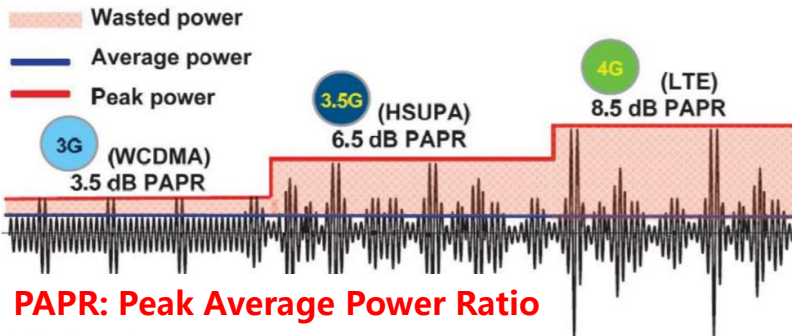
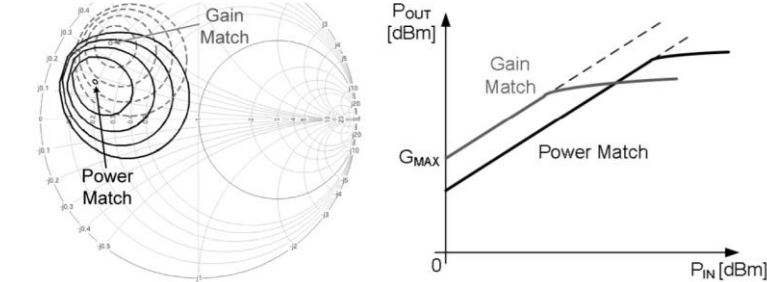


670 GHz Radar Wind-meter (0.2 m/s resolution)

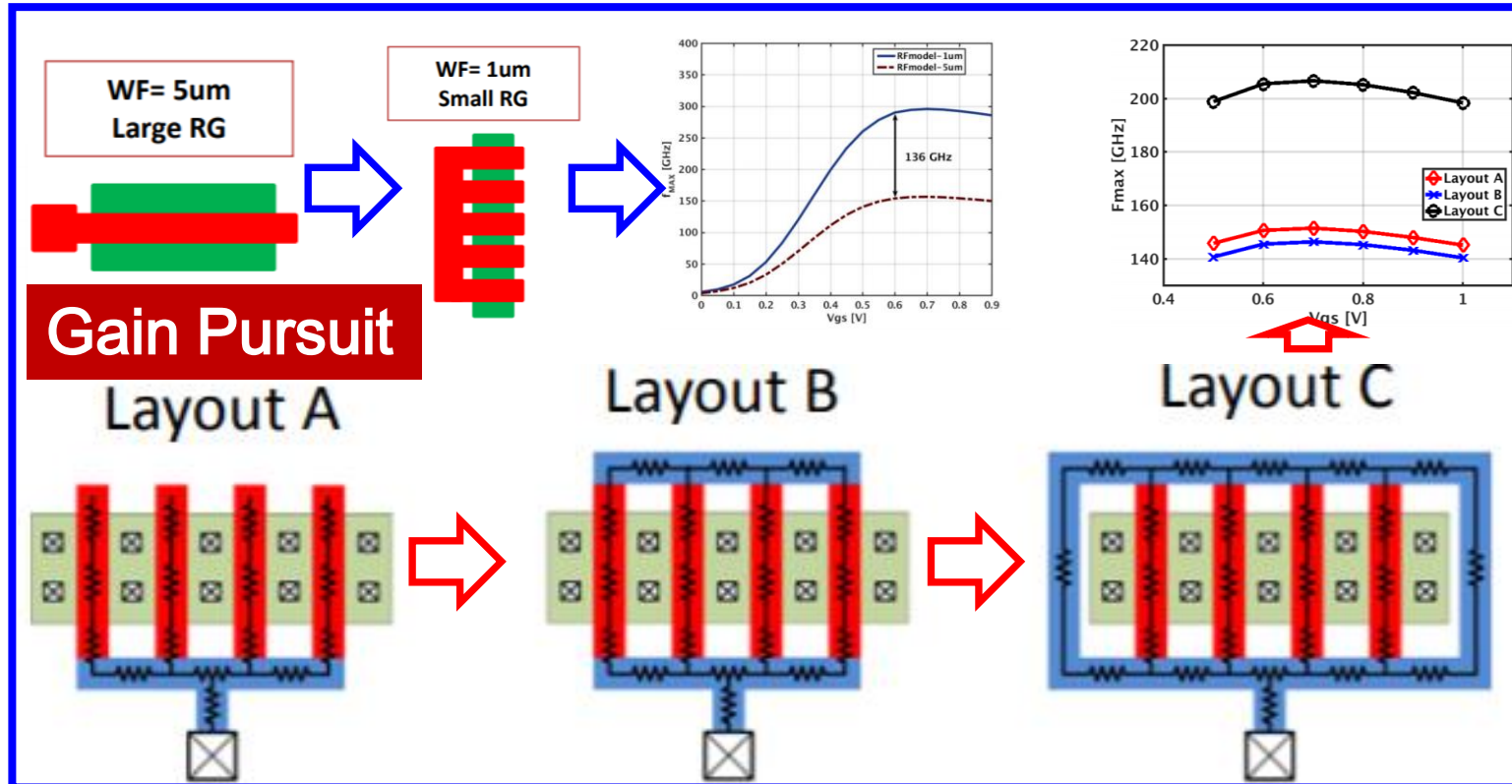


CMOS: MMW Challenges and Accomplishments

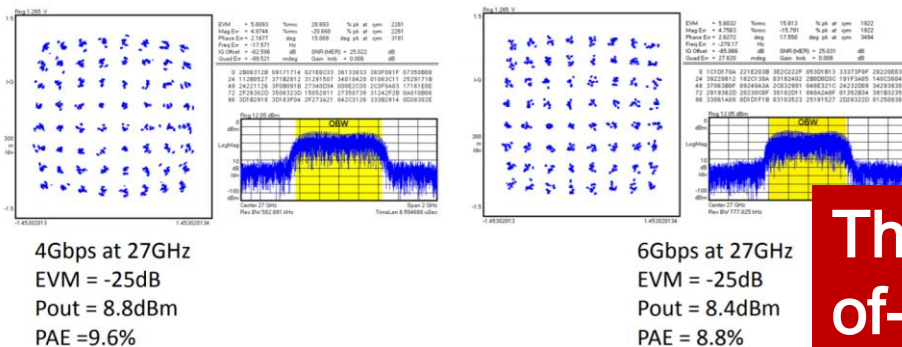
Enhanced PAPR for Spectrum efficient coding schemes like QAM and OFDM



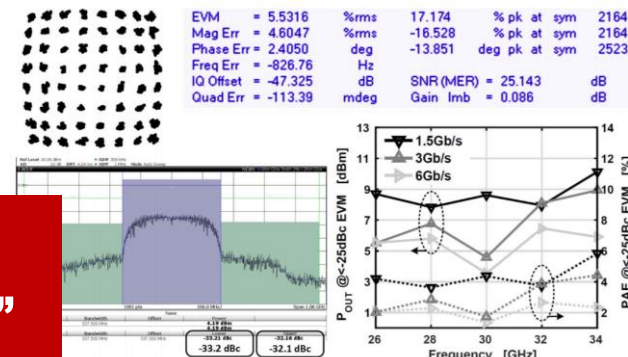
PAPR: Peak Average Power Ratio



KU Leuven: 40nm 28GHz CMOS PA



KU Leuven: 28nm 29-57GHz CMOS PA



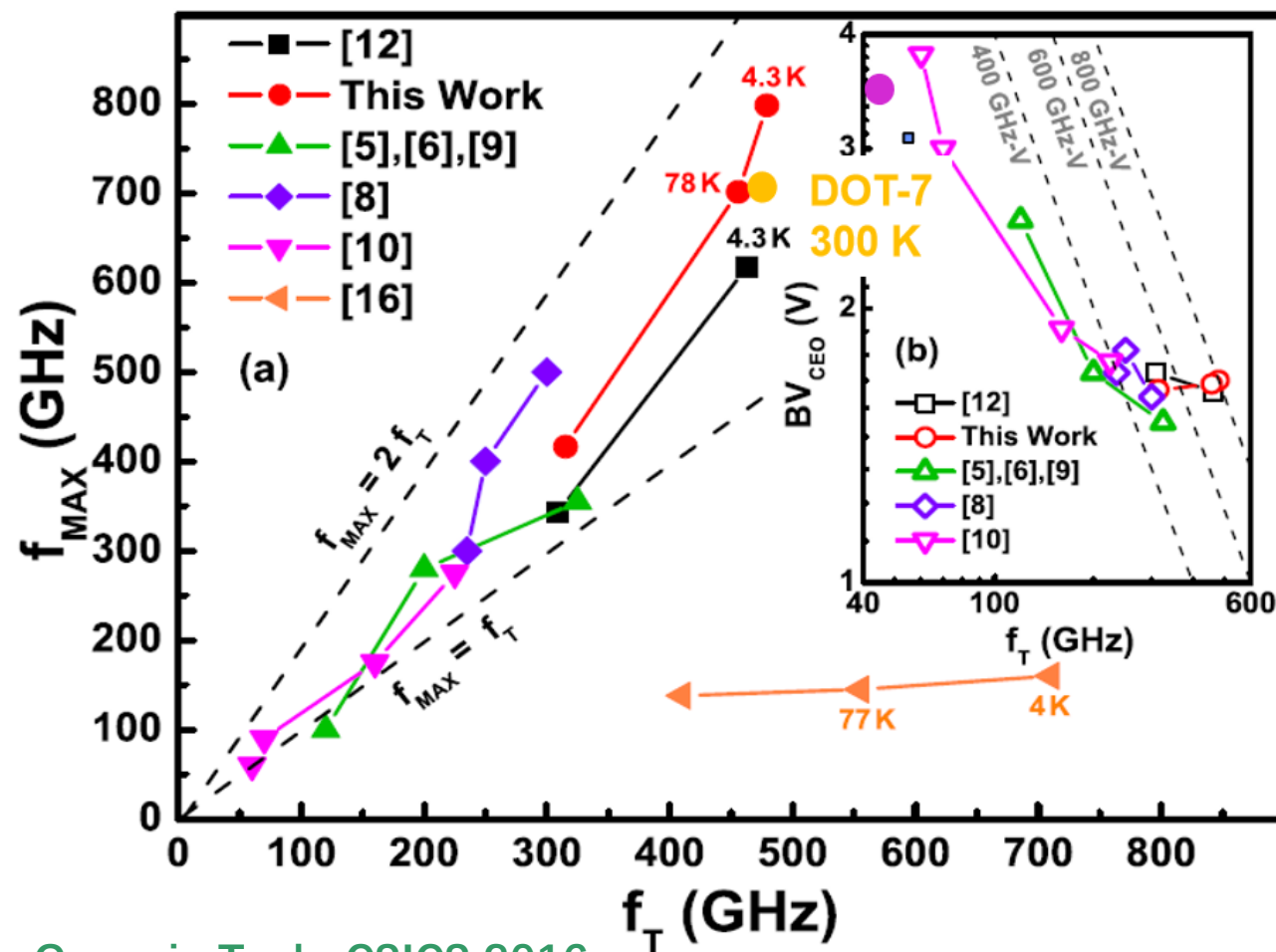
64-QAM, 8.3dB PAPR, no predistortion

P_{OUT} = 10.1dBm
 V_{DD} = 0.9V
 F = 34GHz
DR = 1.5Gb/s

The "State-of-the-ART"

More MMW Powerful SiGe BiCMOS

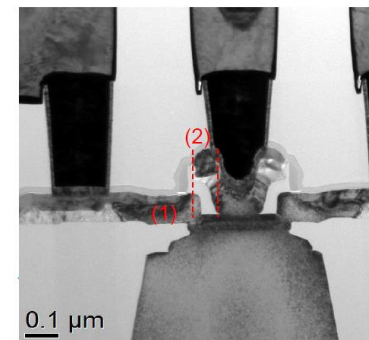
- $f_T + f_{\max} > 1$ THz in SiGe Is Clearly Possible (at very modest lith)
- **The Prediction : 1000 GHz f_{\max} at 300K @ $BV_{\text{CEO}} > 1.5$ V**



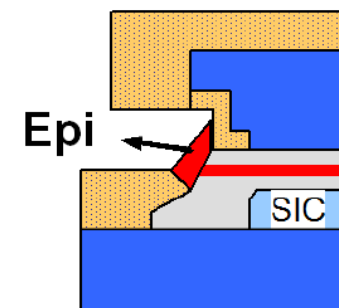
709 GHz @ 300K
800 GHz @ 4.3K
both @ 130 nm

Georgia Tech: CSICS 2016

Cross Section of EBL HBT



[Fox et al., EDL 2015]



Epitaxial base link

- in-situ doped epitaxial external base
- SiGe base & base link formation decoupled
- No link anneal

BiCMOS: To Enable More Low-Cost MMW Applications

Optical networking & Instrumentation
Long haul, // optics, FFTH,... & ADC/DAC,...



100 Gb/s

Wireless HDMI, C2C & P2P links
HDTV, STB, PCs, Smart Phones,...



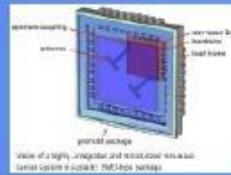
60 GHz

Automotive radars
Cruise speed control & Pre-crash



77* GHz

Industrial & Automotive sensors
Velocity & position sensors



120 GHz

Medical & Security
Imaging (active & passive)



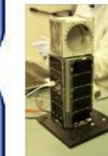
≥160 GHz

SiGe for Radiometers

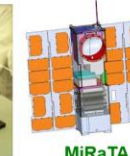
- **Spaceborne Passive Sensors for Profiling the Earth's Atmosphere**
 - temperature (60 GHz, 118 GHz, ...)
 - precipitation and humidity (183 GHz, 325 GHz, ...)
- **Advantages of SiGe for CubeSat Radiometers:**
 - Much better 1/f noise than III-V HEMTs → **critical for radiometers!**
 - Inherent total-dose radiation tolerance as fabricated
 - Integration and economy of scale

[CubeSat Radiometer Demos](#)

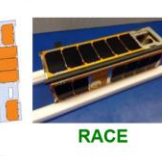
Goal: Large Constellations



MicroMAS



MiRaTA



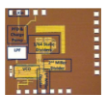
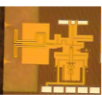
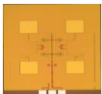
RACE



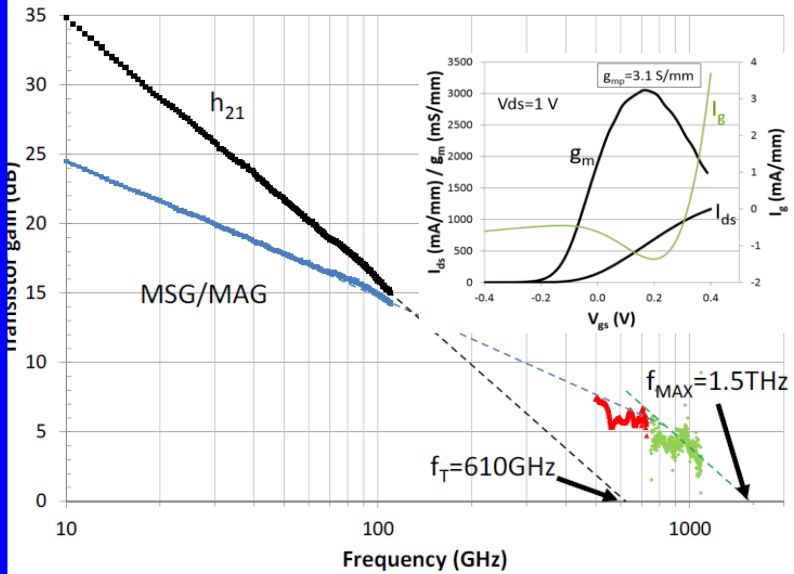
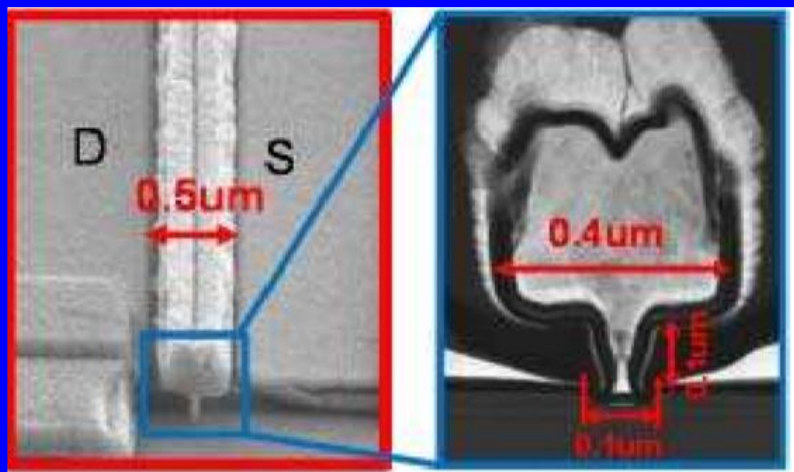
Other Interesting SiGe Circuits ...

- **316 GHz 2x2 Transmitter Array**
 - Novel Locking Mechanism
- **314 GHz Transmitter/Receiver**
 - Low Noise Figure (19.5 dB)
- **107-123 GHz Synthesizer**
 - High Output Power (15 dBm)
 - Low Phase Noise (-95 dBc/Hz at 1 MHz)

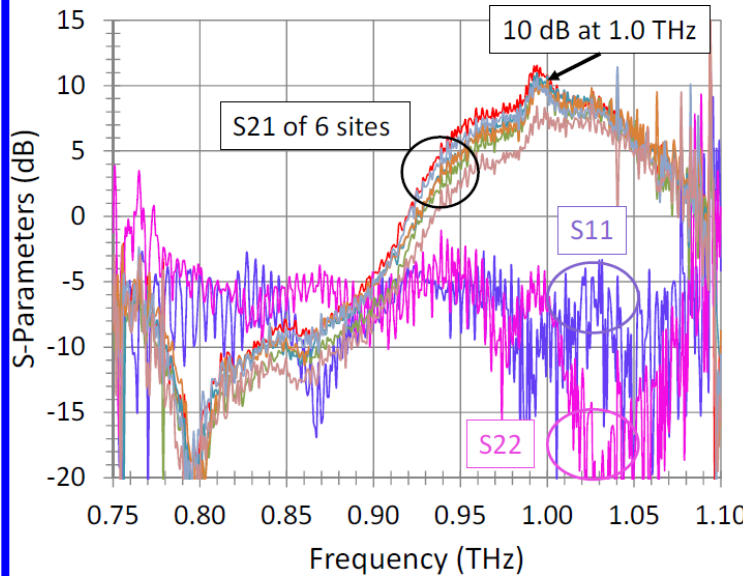
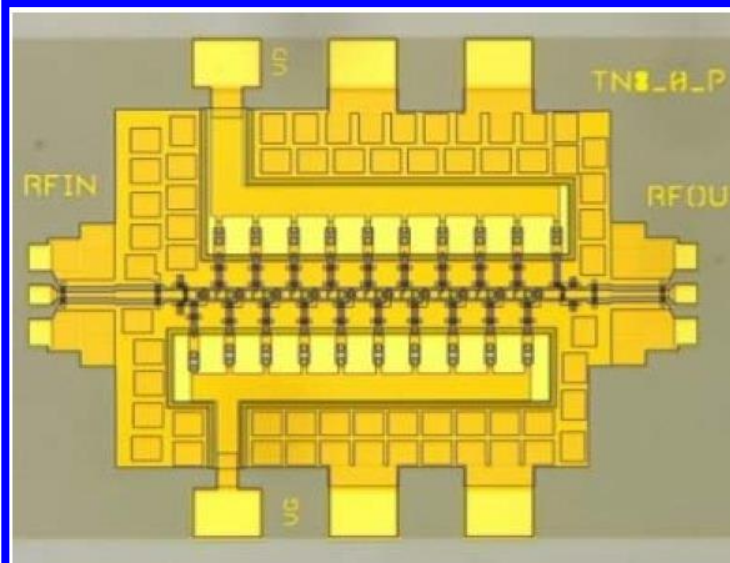
IHP G2



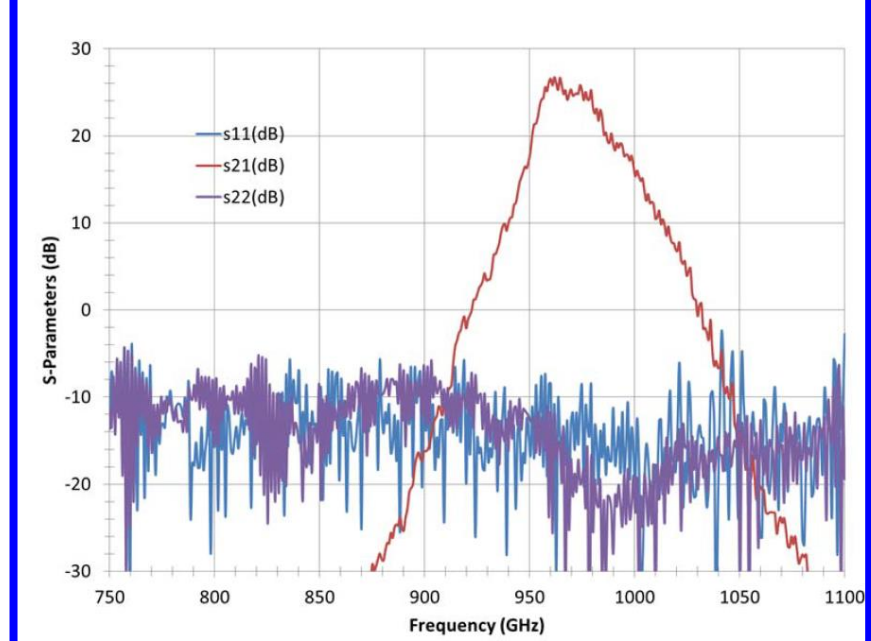
InP HEMT: 25nm Device for THz Operation



**Tera-FET: 95A InAs channel, $L_g=25$ nm
~20 μ m substrate, 4 μ m back via,
 $G_m=3$ S/mm, $f_T/f_{max}=0.61/1.5$ THz**



10-stage TMIC: 10dB @ 1THz

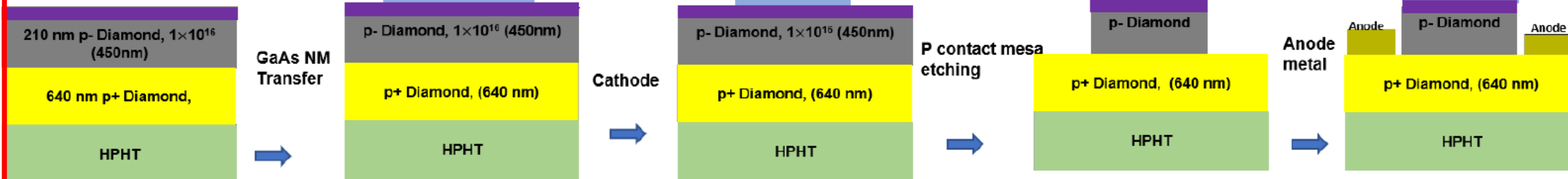


4 cascaded amplifiers: 16dB @ 1THz

AlGaAs/GaAs HBT with Diamond Collector

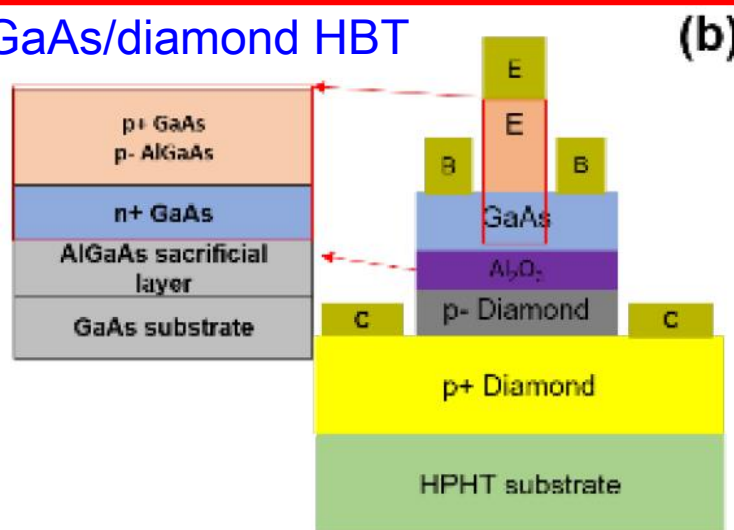
GaAs P⁺/P- diamond
Schottky Diode cleaning

Al₂O₃ deposition

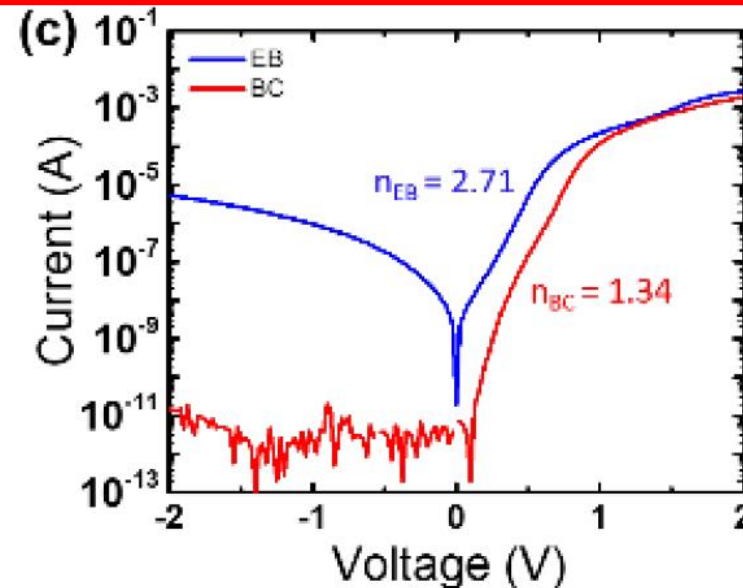
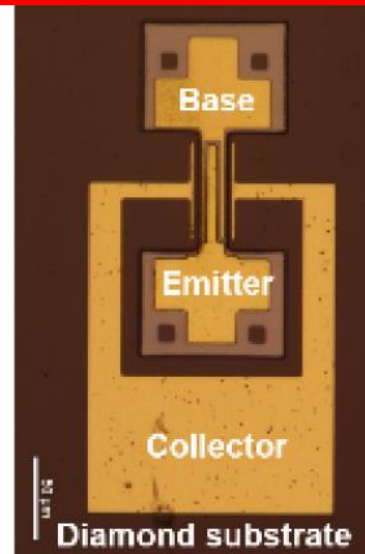


Univ. of Wisconsin: BCICTS 2019

pnp AlGaAs/GaAs/diamond HBT

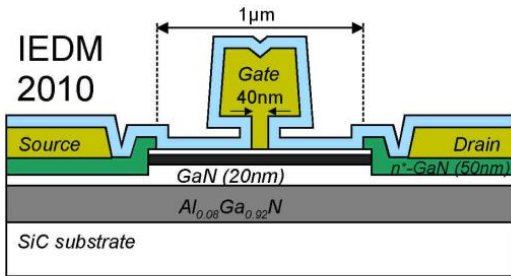


(b)

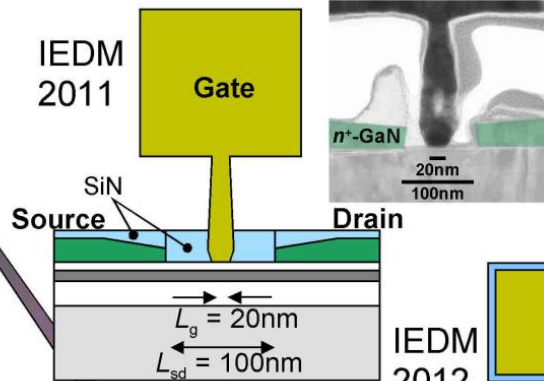


Univ. of Wisconsin: BCICTS 2019

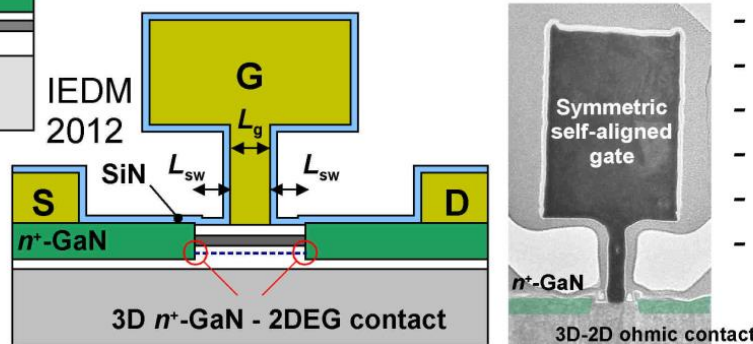
GaN/SiC HEMT: Review of "The-State-of-the-Art"



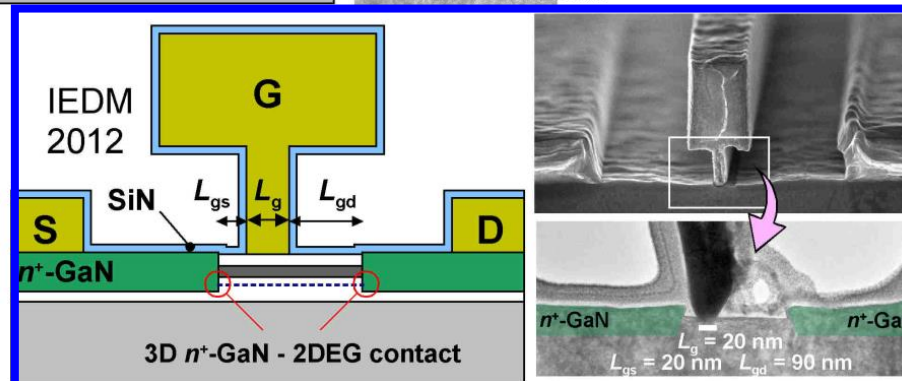
- Vertical epi scaling
- n^+ GaN ohmic regrowth
- 40-nm T-gate ($L_{sd} = 1\mu m$)
- $F_t/F_{max} = 220/400$ GHz, $JFoM = 9.2$ THz-V (D-mode)
- First G-band GaN-MMIC (>4.5 dB gain at 180-200 GHz)



- Self-aligned-gate ($L_g=20nm$, $L_{sd}=100nm$)
- Velocity enhancement by lateral scaling
- $F_t/F_{max} = 310/364$ GHz, $JFoM = 2.8$ THz-V (D)
- $F_t/F_{max} = 343/236$ GHz, $JFoM = 4.0$ THz-V (E)
- 9-stage ring oscillator



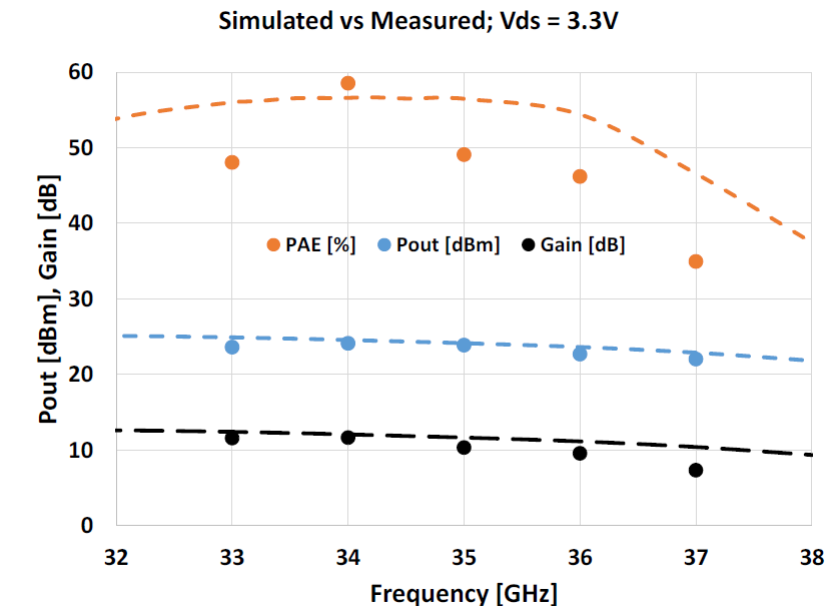
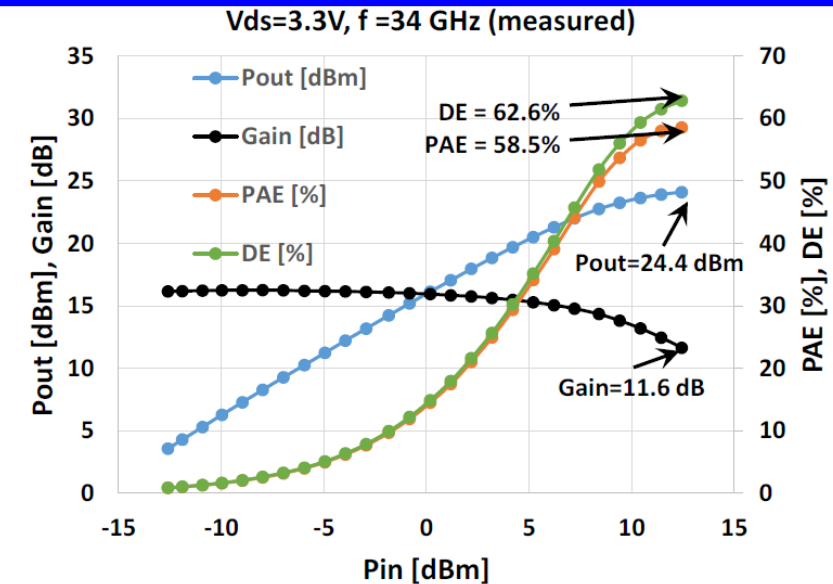
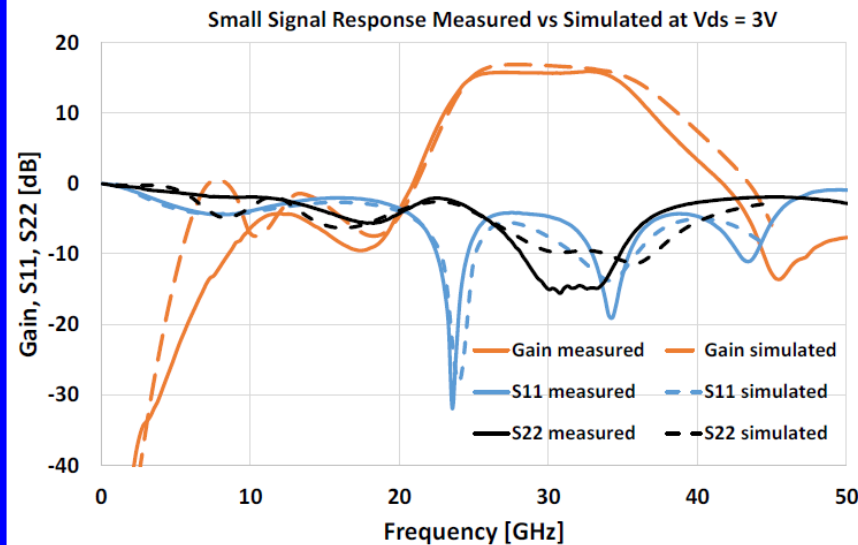
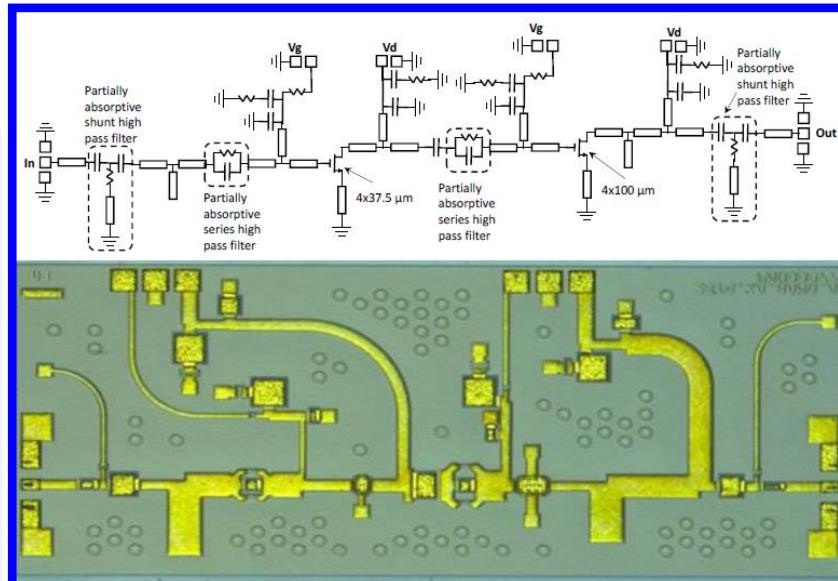
- 3D n^+ GaN ohmic contact to 2DEG
- Impact of L_{sw} on DC/RF performance
- $F_t/F_{max} = 454/444$ GHz, $JFoM = 4.5$ THz-V (D)
- $F_t/F_{max} = 342/518$ GHz, $JFoM = 4.8$ THz-V (E)
- 51, 501-stage ring oscillator
- 3-level BCB interconnect



- Asymmetric self-aligned-gate
- $F_t/F_{max} = 329/582$ GHz, $JFoM = 5.6$ THz-V (MBE, D)
- $F_t/F_{max} = 200$ GHz at $V_{ds}=0.2V$
- $NF_{min} = 0.8$ dB (50GHz, 20mW) = 1.0 dN (50GHz, 6mW)

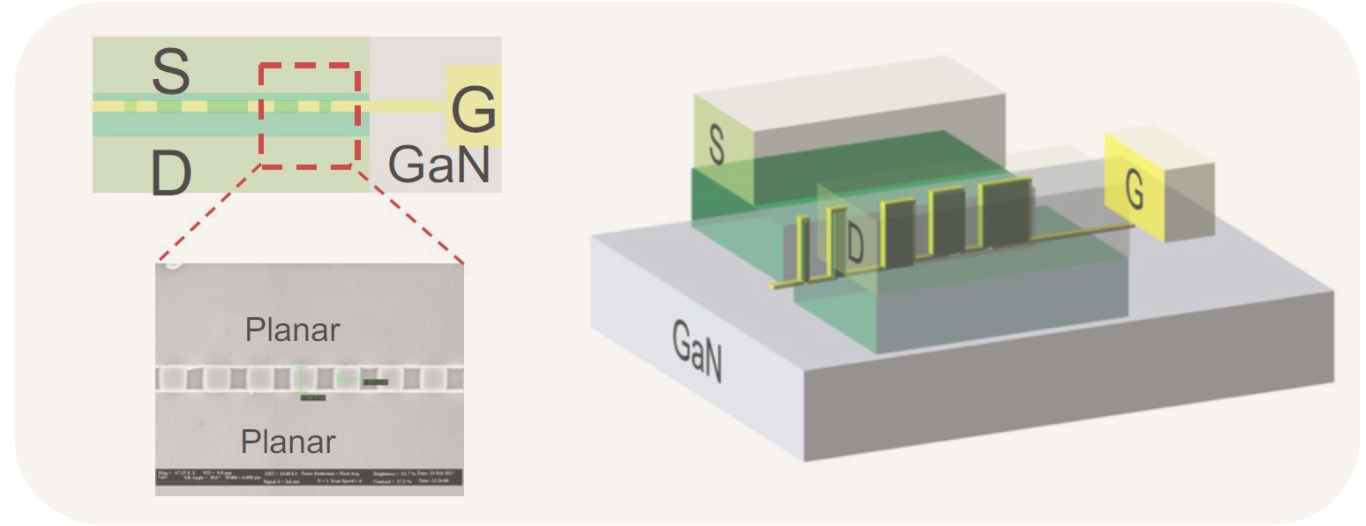
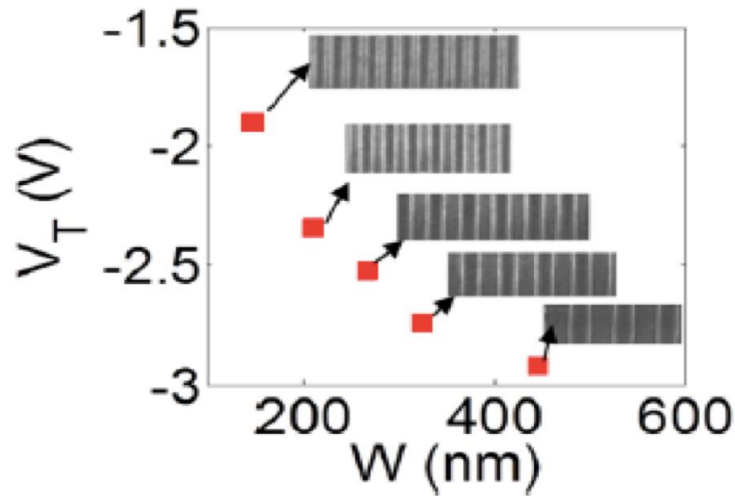
Ka-Band GaN/SiC PA with 58.5% PAE

- HRL's 40 nm T4A GaN process: $f_t > 300$ GHz, $f_{max} > 500$ GHz, and $BV > 15$ V
- 2 stage MMIC PA: 1st stage: 4×37.5 μm , 2nd stage: a 4×100 μm
- Highest attainable PAE using a scalable non-linear Angelov HEMT model extracted from prior wafers
- Designed in MWO environment
- PAE of 58.5% with associated gain of 11.9 dB and associated Pout of 24.4 dBm at 34 GHz under CW

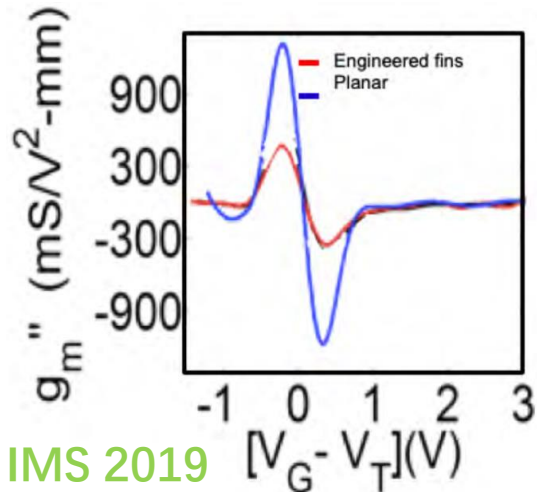


GaN/SiC HEMT: High-Linearity FINFET

FINFET with different widths corresponds different V_p

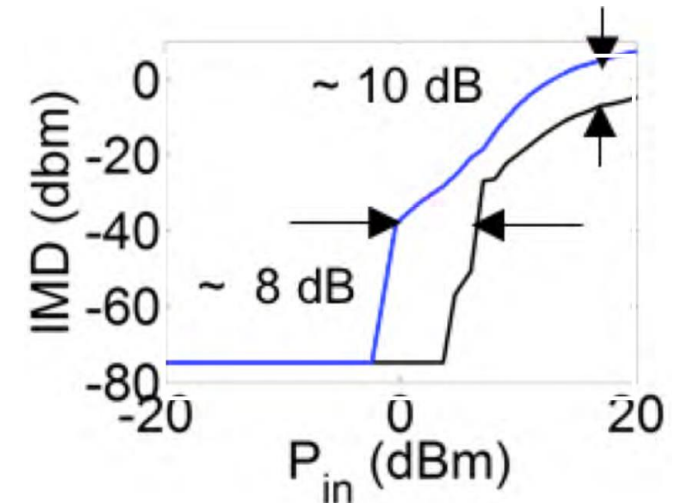


Superimposition of properly designed FETs leads to reduced g_m'' and improved IMD



Block diagram showing the input v_{in} (freq) entering a Power Amp, resulting in the output v_{out} (freq) with additional frequency components (IMD).

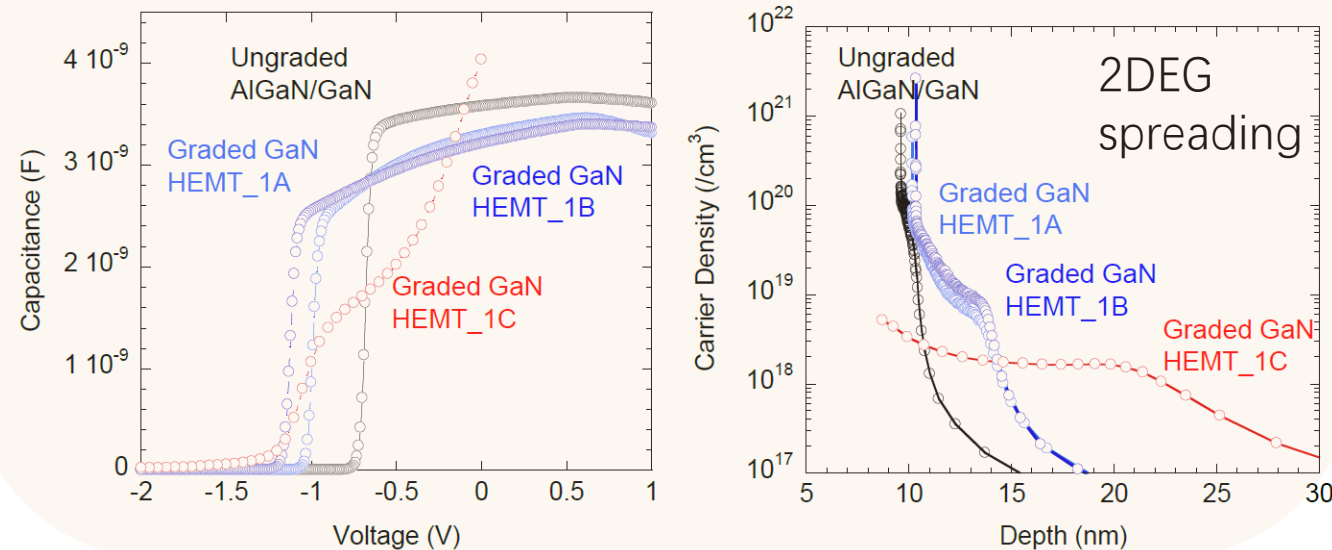
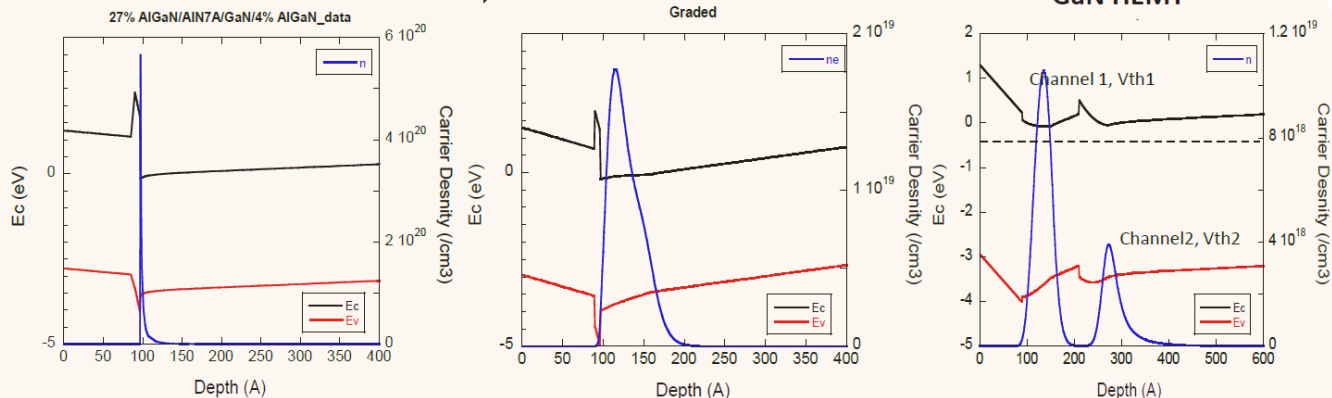
$$IMD \propto \frac{g_m'' A^3 (3 \cos((2\omega_1 - \omega_2)t) + 3 \cos((2\omega_2 - \omega_1)t))}{4}$$



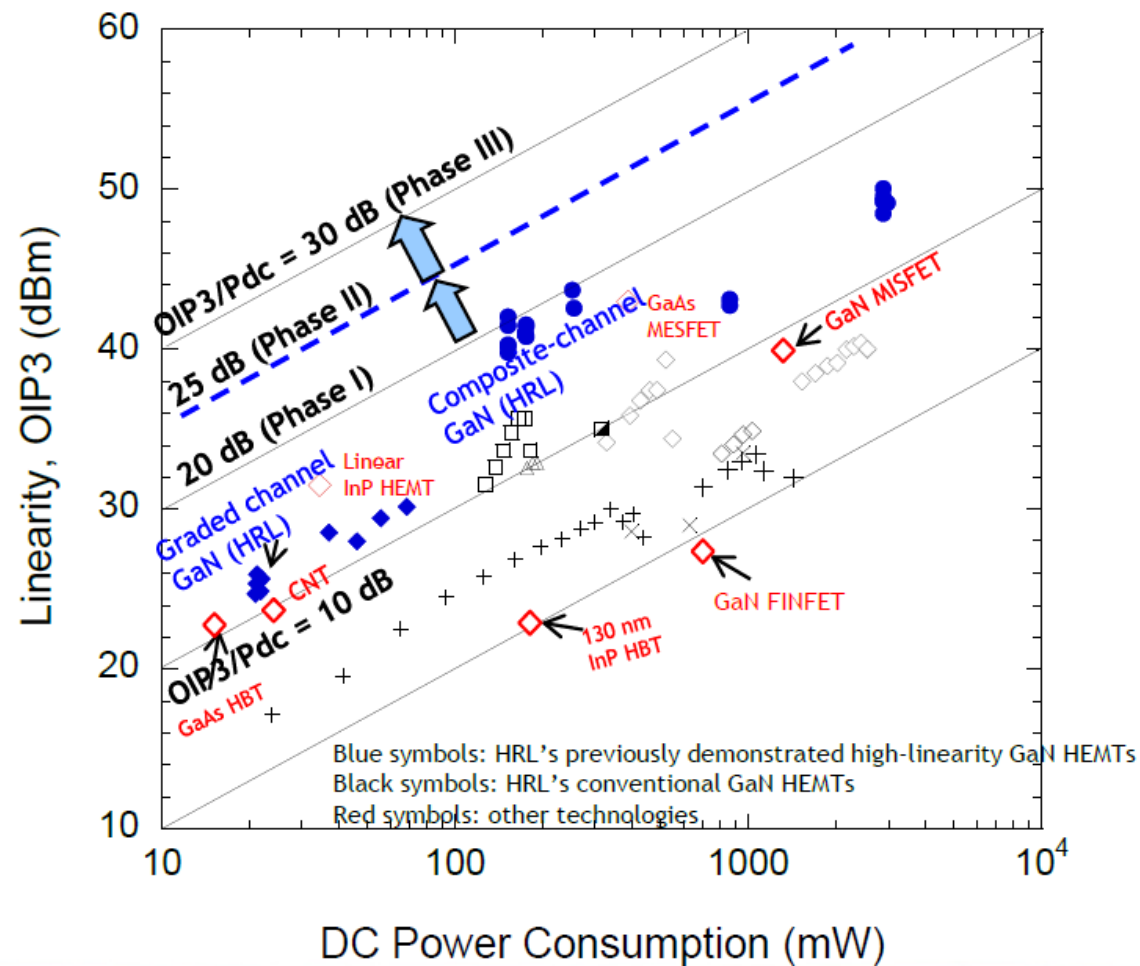


GaN/SiC HEMT: High-Linearity with Graded Channel

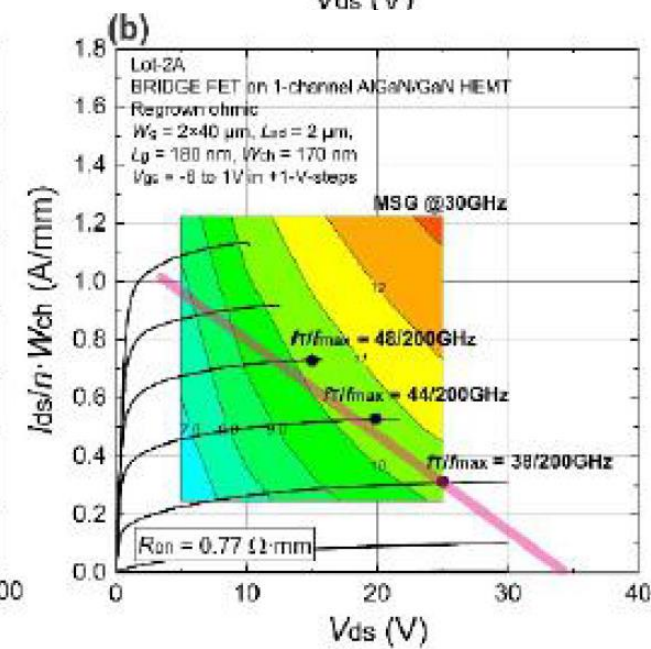
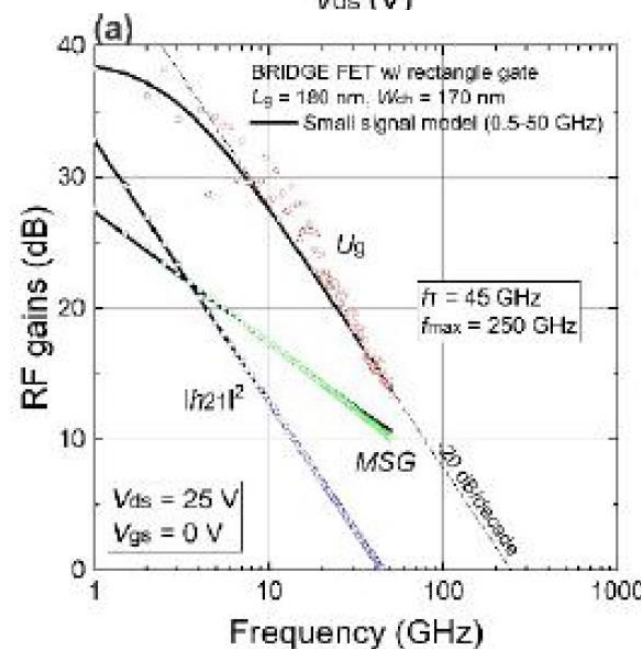
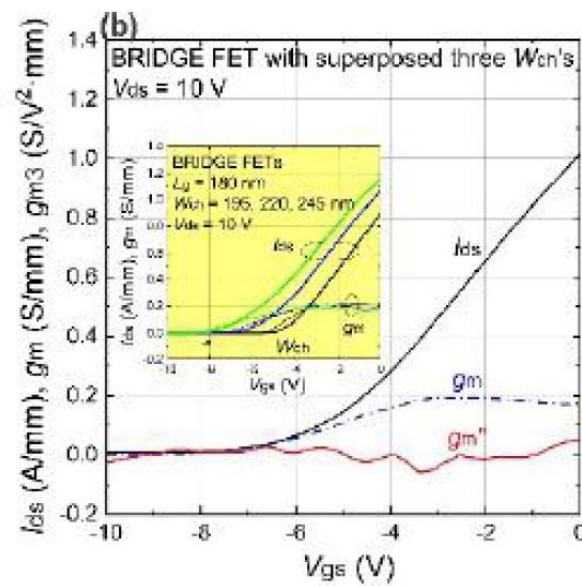
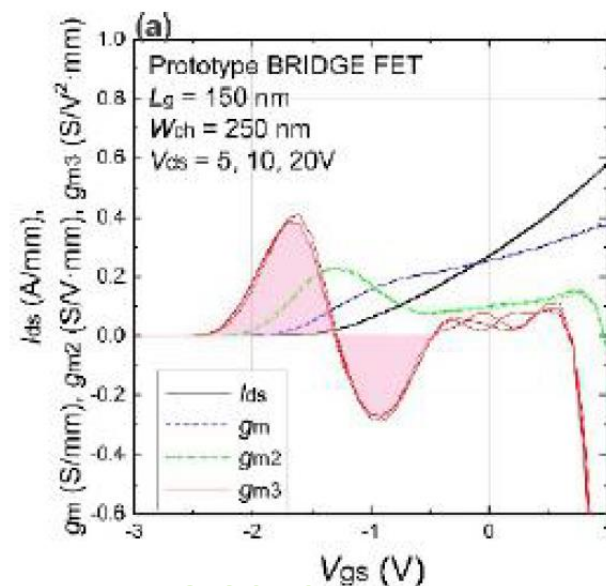
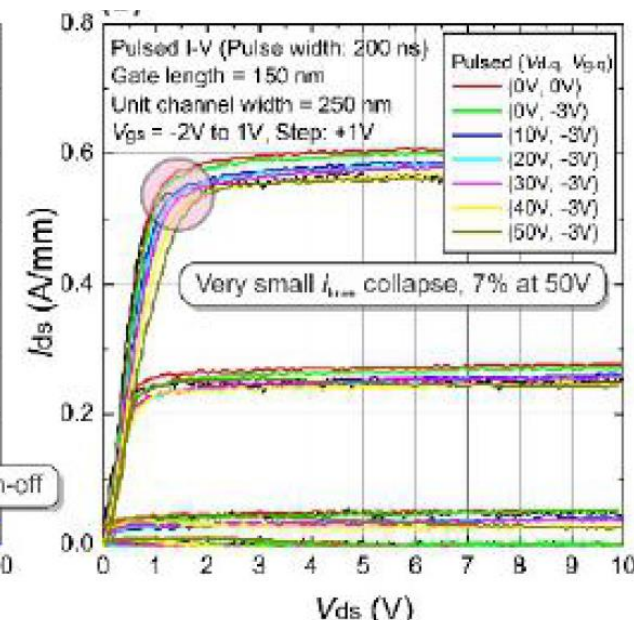
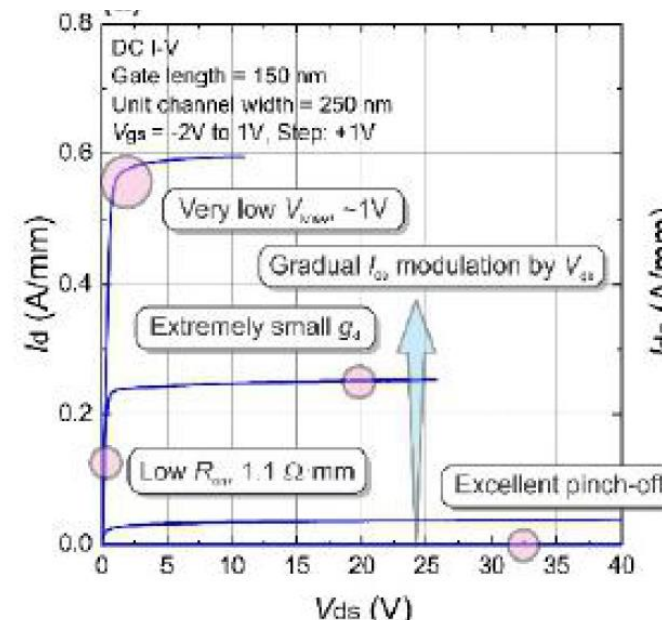
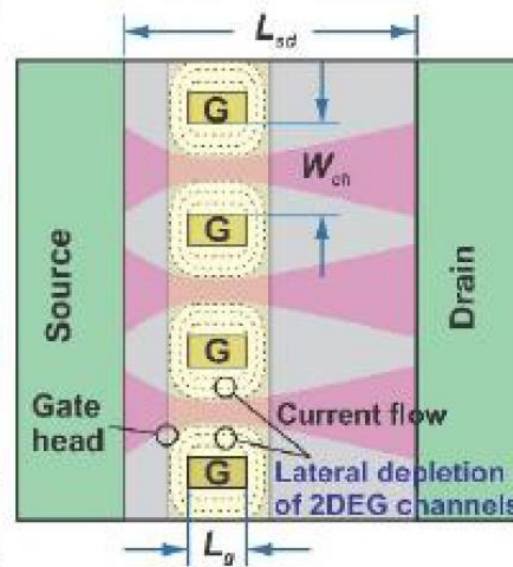
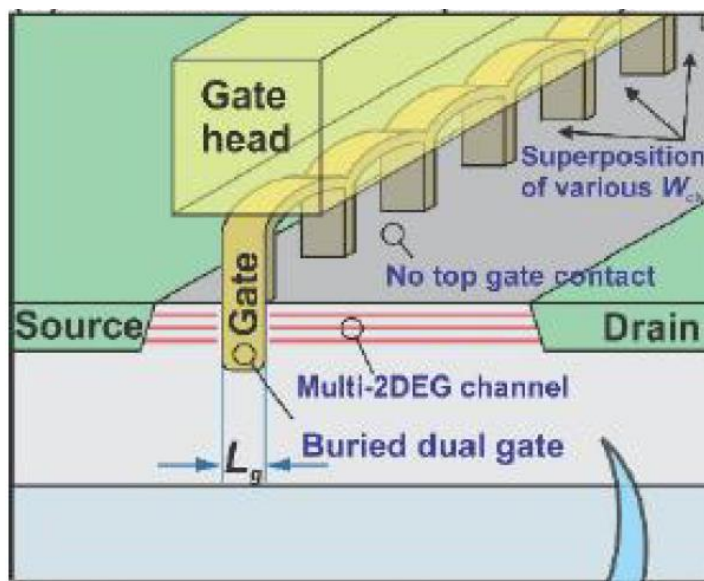
Conventional AlGaIn/GaN HEMT → Graded-channel GaN HEMT → Graded & Composite channel GaN HEMT



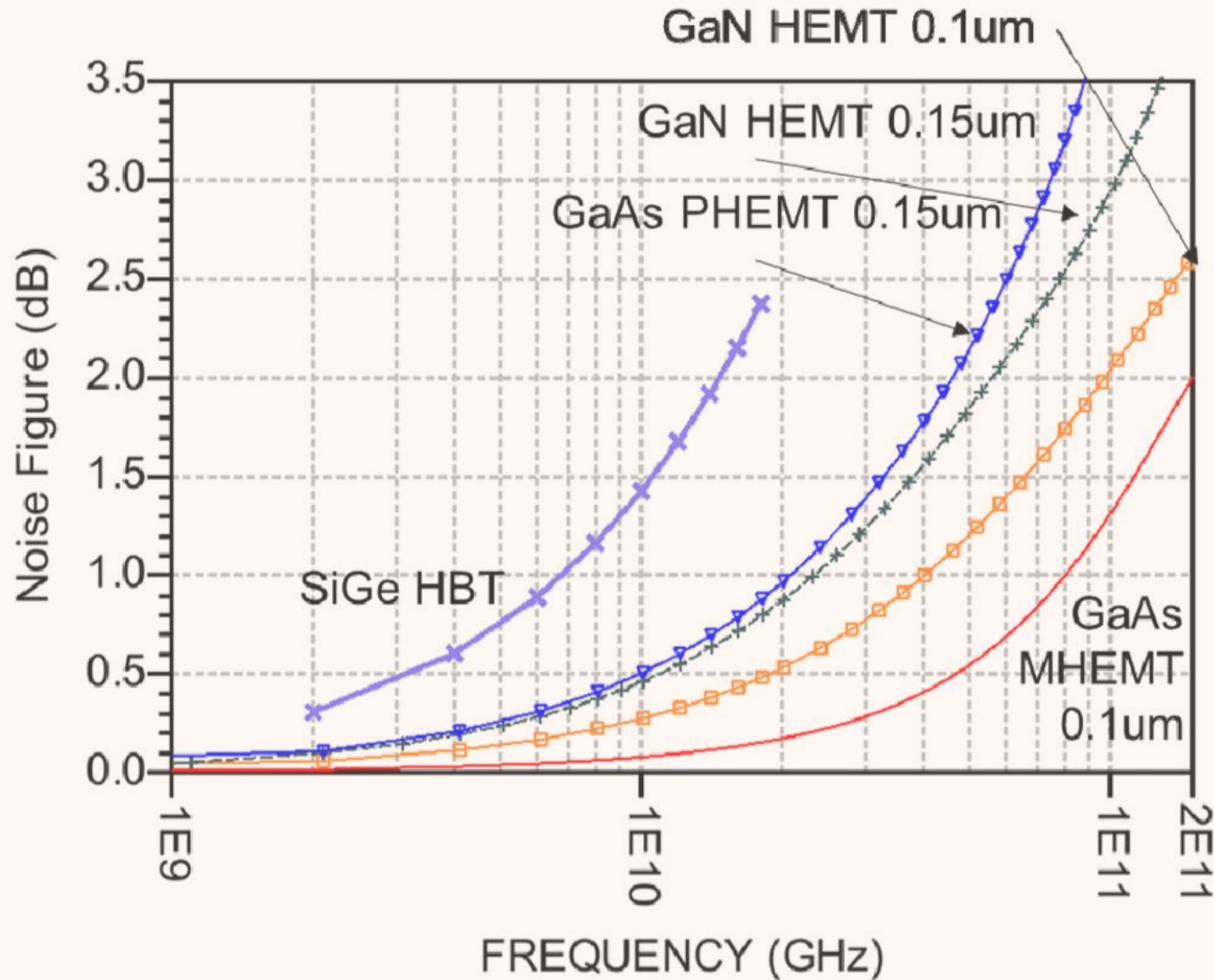
DARPA DREAM: Ultra linear mmw GaN HEMT with $\text{OIP}_3/P_{\text{DC}} > 30\text{dB}$ at 30GHz



Buried Gate GaN/SiC HEMT with High Linearity



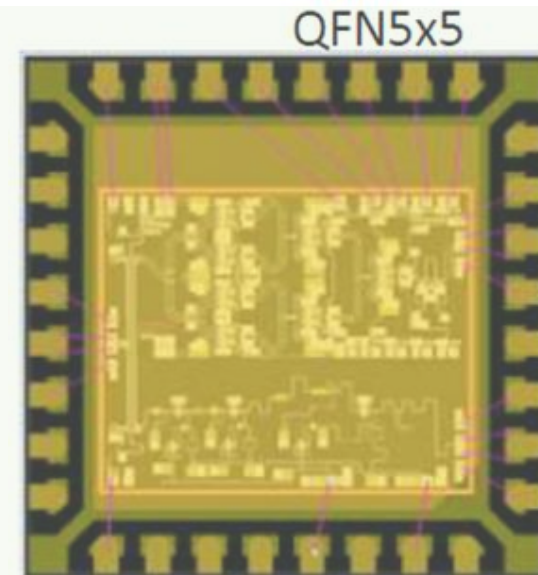
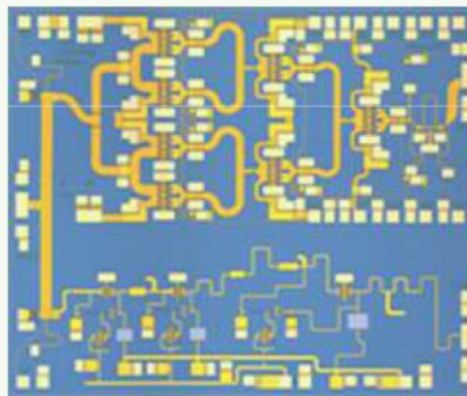
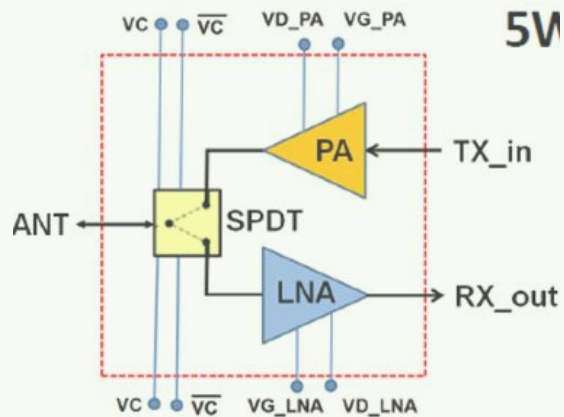
GaN/SiC HEMT for LNA Applications (up to W Band)



- 0.15μm GaN HEMT: Surprisingly low F_{\min} typically ~3dB @W band
- Competitive versus (even better than) InGaAs based PHEMT, but with >5x ruggedness to incoming power to avoid adopting limiter design
- Another ~0.8dB F_{\min} improvement achieved when using 0.1μm GaN HEMT
- GaN HEMT F_{\min} next only to InP MHEMT with similar gate lengths

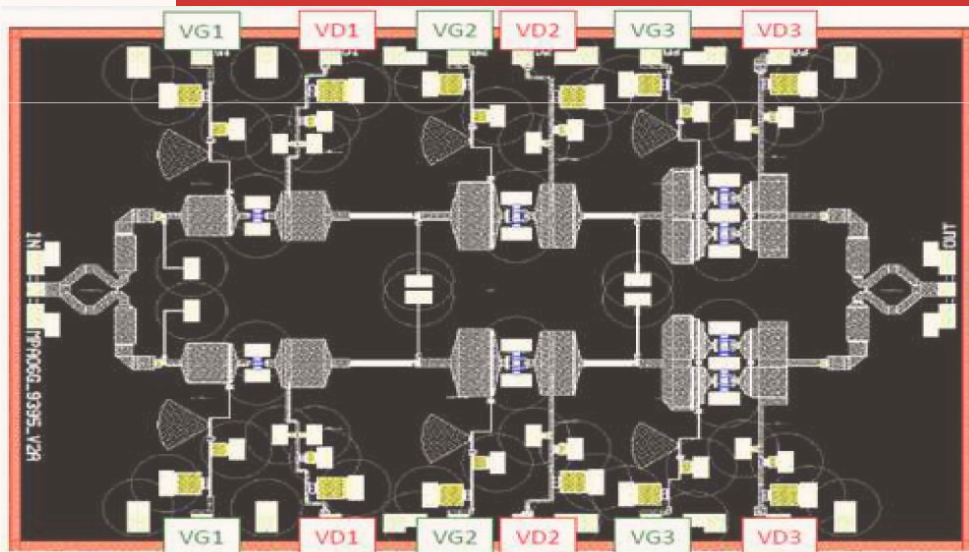
GaN/Si HEMT Lower Cost

100nm GaN/Si Based TR for Ka Band



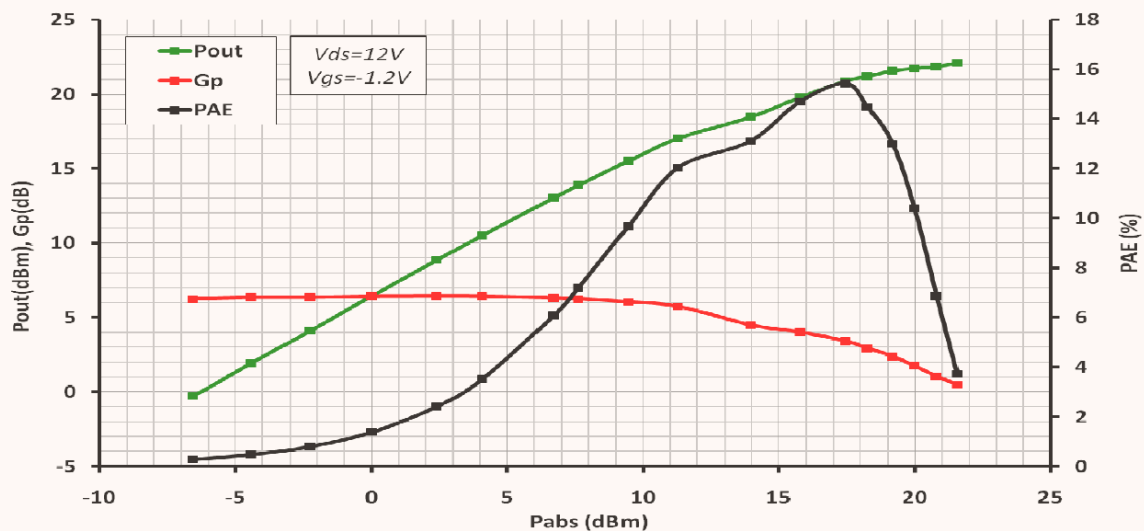
OMMIC: EMWW 2018

60nm GaN/Si Based PA for W Band



600mW, 12dB gain, 10%PAE @ 94GHz, 12V

Load Pull @94GHz [$2 \times 35\mu\text{m}$ $L_g=60\text{nm}$ $L_g=250\text{nm}$]



THANK YOU

More than Moore • More than Innovation



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